

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No7,087,478 B2
Patent Issue Date August 8, 2006
Application Serial No. 10/624,627
Filing Date July 21, 2003
Assignee Micron Technology, Inc.
Inventorship Tran
Attorney's Docket No. MI22-2358
Title: Methods of Forming Semiconductor Constructions

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR
APPLICANT MISTAKES and PTO MISTAKES (37 C.F.R. §§ 1.322(a) and 1.323)**

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Decision and Certificate of Correction
Branch of the Patent Issue Division

From: Jennifer J. Taylor, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 7,087,478 B2, granted August 8, 2006, in accordance with the Certificate of Correction form attached hereto.

It is noted that errors appear in this patent of a typographical nature of character, as more fully described below. The errors occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require re-examination.

Other errors listed on the Certificate of Correction form were apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.

Attached hereto is Form PTO-1050 that is suitable for printing.

The exact page and line number where the errors occur in the application file are:

Paragraph 5, Line 4

Paragraph 91, Line 4

Paragraph 51, Line 2

Paragraph 94, Line 8

Paragraph 52, Line 2

Paragraph 97, Line 12

Paragraph 56, Line 6

Paragraph 102, Line 8

Paragraph 57, Line 6

Paragraph 116, Line 4

Paragraph 86, Line 4

Deposit account 23-0925 is to be charged the amount of \$100.00, as required by 37 CFR 1.20(a).

Dated: 3/29/07

Respectfully submitted,

By: Jennifer J. Taylor
Jennifer J. Taylor, Ph.D.
Reg. No. 48,711

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 7,087,478 B2

DATED : August 8, 2006

INVENTOR(S) : Tran

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, Line 63 – Replace “Ga or TI” with --Ga or Tl--.

Column 5, Line 6 – Replace “source drain regions” with --source/drain regions--.

Column 5, Line 23 – Replace “can comprises” with --can comprise--.

Column 5, Line 61 – Replace “alter electrically” with --alter electrical--.

Column 6, Line 8 - Replace “Ga or TI” with --Ga or Tl--.

Column 10, Line 11- Replace “some Portion” with --some portion--.

Column 10, Line 43 – Replace “of the of the” with --of the--.

Column 11, Line 15 – Replace “pattern d” with --patterned--.

Column 11, Line 50 – Replace “center d” with --centered--.

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Mailing Address of Sender:

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Patent No. 7,087,478 B2

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 7,087,478 B2

DATED : August 8, 2006

INVENTOR(S) : Tran

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12, Line 57 – Replace “source drain region” with --source/drain region--.

Column 14, Line 56 – Replace “can b” with --can be--.

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Patent No. 7,087,478 B2